GOVERNMENT POLYTECHNIC, DHENKANAL DEPARTMENT OF ELECTRONICS AND TELECOMMUNICATION ENGG. LESSON PLAN SUMMER 2025

		LESSON PLAN SUMMER 2025
Subject:- Analog	No of Days/per	Semester From:- :- <u>04.02.2025</u> To:- 17 <u>.05.2025</u>
Electronics &	Week Class	
Linear IC	Allotted :-	
(Th. 4)	5	Name of the Faculty: Alshwarya Dash
Week	Class Day	Theory
1 st	1 st	Working principle, of Diode & its current equation, Specification and use of
	2 nd	Breakdown of diode (Avalanche & Zener Breakdown) and Construction,
	3 rd	Classification of Rectifiers and working of different types of Rectifiers- Half- Wave Rectifier,
	4 th	, Full-Wave Rectifier (CT & BRIDGE type)
	5 th	Working principle of p-n-p and n-p-n transistor, different types of transistor
2nd	1 st	input and output characteristics of transistor in different connections.
	2 nd	Define ALPHA, BETA and GAMMA of transistors in various modes.
	3 rd	Basic concept of Biasing, Types of Biasing, h-parameter model of BJT, load
	4 th	Types of Coupling, working principle and use of R-C Coupled Amplifier
	5 th	Frequency Responses of R-C coupled Amplifier & draw the curve.
	1 st	Classify Power Amplifier &Differentiate between Voltage and Power Amplifier
	2 nd	Working principle of different types of Power Amplifier (Class-A)
3rd	3 rd	Working principle of different types of Power Amplifier (Class-B)
	4 th	Working principle of different types of Power Amplifier (Class -AB)
	5 th	Working principle of different types of Power Amplifier (Class-C)
4th	1 st	Working principle of different types of Power Amplifier (Class-D)
	2 nd	Construction and working principle and advantages of Push Pull (Class-B) Amplifiers
	3 rd	Construction and working principle and advantages of Push Pull (Class-B) Amplifiers
	4 th	FET & its classifications.
	5 th	Differentiate between JFET & BJT
5th	1 st	Construction, working principle & characteristics of JEFT
	2 nd	Explain JEFT as an amplifier, parameters of JFET
	3 rd	Establish relation among JFET parameters
	4 th	Construction & working principle MOSFET & its classification
	5 th	& characteristics of MOSFET (Drain & Transfer)
6th	1 st	Explain the operation of CMOS
	2 nd	Explain the operation of VMOS
	3 rd	Explain the operation of LDMOS
	4 th	Define & classify Feedback Amplifier, principle of negative feedback with the help of block diagram,
	5 th	Types of feedback – negative &positive feedback
	1 st	Types of negative feedback – voltage shunt, voltage series,
	2 nd	
	2	current shunt& current series and characteristics voltage gain, bandwidth, input Impedance output impedance,
7th	3 rd	stability, noise, distortion in amplifiers
	4 th	-
	4	Oscillator -block diagram of sine wave oscillator, Types Requirement of oscillation Barkhausen criterion

4.	5 th	RC oscillators - RC phase shift ,Crystal, LC oscillators - Colpitts
	1 st	Hartley & Wien Bridge Oscillators : Circuit operation, circuit diagram
	- 20	equation for frequency of oscillation & frequency stability
2	2 nd	Defined and classify Tuned amplifier, Explain parallel Resonant circuit,
8th	3 rd	Resonance Curve & sharpness of Resonance
	4 th	working principle of Single tuned Voltage& Double tuned Amplifier & its
		limitation
	5 th	Different type of Non-linear circuits - Clipper,
9th	1st	diode series &shunt, positive& negative biased
	2 nd	unblased and combinational clipper clippers giravit & its application
	3 rd	The control of Clamper Circuit (positive of mosetime alama v o the
	4 th	VI TISLAUIC. WININGIANIA
	5 th	Monostable Multivibrator with circuit diagram
	1 st	Distable Multivibrator - '11 :
10th	2 nd	explain its configuration & significance
10th	3 rd	Block diagram representation of a typical Op- Amp
	4 th	its equivalent circuits and draw the schematic symbol
	5 th	Discuss the types of interest lain draw the schematic symbol
-	1 st	Discuss the types of integrated circuits manufacturer's designations of ICs, pin identification and temperature and and in its control of the
	2^{nd}	
11th	3 rd	TOTO TOTO PIECTFICOL Oboms -4
11(1)	4 th	
Ì	5 th	and explain tile Open Loon configuration (
2.	3	
	ot .	the close loop Voltage gain,
_	1 st	gain of feedback circuits input registeres.
12th		and total output offset voltage with feedback.
	2 nd	Draw the circuit diagram of the voltage shunt feedback.
	3 rd	derive the close loop, Voltage gain,
	4 th	gain of feedback circuits and input registeres
1.5	5 th	gain of feedback circuits and input registers
13th	1 st	Discuss the summing scaling and averaging of inverting and non-inverting
	2 nd	DC & AC Amplifies using OP-AMP
	3 rd	Integrate were in the state of
	4 th	Integrator using op-amp
F	5 th	differentiator using op-amp
l	5	Active filter and describe the filter design of fast order low Pass Butterworth
14th	1 st	Concept of Zero-Crossing Detector using Op-Amp
	2 nd	Detector using Op-Amp
	3 rd	Block diagram and operation of IC 555 timer
	4 th	IC 565 PLL & its applications
	5 th	Working of Current to voltage C
15th	1 st	Working of Current to voltage Convertor using Operational Amplifier
	2 nd	or the voltage to Fredliency Convertor using Operational
		Working of the Frequency to Voltage Conversion using Operational Amplifier
	3 rd	
	4 th	Operation of power supply using 78XX Operation of power supply using 79XX
	5 th	Operation of power supply using V9XX Operation of power supply using LM 317

Signature of Faculty

Signature of HOD